

Homework IV

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Due Mar. 3, 2004

1. 4.13. Give a specific algorithm for adjusting A_{upper} .
2. 4.14.
3. 4.16.
4. 4.18.
5. 4.48. Illustrate the delay model you use for the one-bit full adder, the four-bit carry lookahead adder, and the carry lookahead unit. Make sure you show your work so that I can determine how you arrive at your delay figures.