

# Sequential Logic

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## Chapter 3, Slides

- Slide 3: What distinguishes combinational logic from sequential logic?
- Slide 5: What is the purpose of the clock signal?  
How does the clock signal relate to a processor's "speed?"
- Slide 10: The Read Logic uses the address input and what chip from the first project to switch the output value of the selected register to `out`?  
The Write Logic uses the address and load inputs and what chip from the first project in order to load the value `in` into the selected register?
- Slide 13: If `inc`, `load`, and `reset` are all high (1), what is the counter's behavior?  
If `inc`, `load`, and `reset` are all low (0), what is the counter's behavior?
- Slide 15: What is the relationship between propagation delay and processing speed?
- Slide 16: Name and describe the four elements of the memory hierarchy and compare them in terms of access time, cost, and amount.