Problem Set 29

CS 411

Due at the beginning of class on the first class day of the following week.
Sections 13.1–3

1. When multiple interrupts from different devices appear at about the same time, a priority scheme could be used to determine the order in which the interrupts would be serviced. Discuss what issues need to be considered in assigning priorities to different interrupts.

2. From the CPU's perspective, how is a DMA controller a benefit, and how is it a hindrance?

3. Why does Linux have its `ioctl()` interface?