

Homework V

Tom Kelliher, CS 220

50 points, due Apr. 17

1. 6-15.
2. 6-21.
3. 6-35. Additionally, you are to download your design to one of the FPGA boards in the X Lab, run a program I will provide to test your circuit, and report the result. Submit the VHDL and your report.
4. Write a VHDL description for the circuit specified in Problem 6-21. Additionally, you are to download your design to one of the FPGA boards in the X Lab, run a program I will provide to test your circuit, and report the result. Submit the VHDL and your report.