

Homework V

Tom Kelliher, CS 220

50 points, due Nov. 12

On pp. 243–247:

1. 4-20.
2. 4-21.
3. 4-42. You are to download your design to one of the FPGA boards in the X Lab, run a program I will provide to test your circuit, and report the result. Submit the VHDL and your report.
4. 4-43. You are to download your design to one of the FPGA boards in the X Lab, run a program I will provide to test your circuit, and report the result. Submit the VHDL and your report.