

# Final Exam Review

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## 1 Administrivia

### Announcements

Comments on semester.

### Assignment

Toolboxes due Friday.

Counters due Monday.

### From Last Time

Finished up RAM.

### Outline

1. Review.

### Coming Up

Final.

## 2 Final Exam Review

Final will be open book, notes, calculator. You may only discuss it with me — no one else. You may not use the Xilinx software while you are working on the exam.

Six questions.

Question topics (not all may be used):

1. Binary arithmetic, including circuits.
2. Combinational circuit design.
3. Sequential circuit design.
4. VHDL.
5. Registers/Counters.
6. Static RAM.